

Exercise 3: (4 pts = 2 (1+1) + 2 (1+1))

Taking the notation of the **simple precision floating point (32 bits)** of the IEEE 754 standard

1) Give the representation in **SP floating point (32 bits)** of the following numbers:

$$-43.625 \times 2^{-107}_{(10)} \quad // \quad +53.75 \times 2^{-134}_{(10)}$$

2) Put in the form $\pm a \times 2^b$ the two following hexadecimal contents:

$$X = 24E00000_{(16)}, Y = 80500000_{(16)} \quad (a \text{ and } 2^b \text{ are decimals})$$

Exercise 4: (7 pts = 1.5+1.5+0.75+0.75+2.5 (1+1.5))

$$F1(X, Y, Z) = (X + Y)Z + \bar{X}(\bar{Y} + Z) + \bar{Y}$$

$$F2(A, B, C) = (A + \bar{B} + \bar{C})(A + \bar{B} + C)(A + B + \bar{C})$$

$$F3(A, B, C, D) = \sum (0, 1, 3, 5, 6, 10, 15) + \Phi (2, 4, 7, 11)$$

1. Simplify **F1** using Algebraic simplification and write the truth table of **F1**.
2. Write the two canonical forms of **F1**.
3. Draw the logigram of **F1** (simplified) only with NAND gates.
4. Simplify **F2** using Algebraic simplification.
5. Simplify **F2** and **F3** using Karnaugh maps in the form of Sum of Products (**SoP**) and Product of Sums (**PoS**).